



# PANIMALAR ENGINEERING COLLEGE

An Autonomous Institution, Affiliated to Anna University, Chennai  
A Christian Minority Institution

(JAISAKTHI EDUCATIONAL TRUST)

Approved by All India Council for Technical Education



## III YEAR END SEMESTER EXAMINATION LAB SCHEDULE APR 24

Date	Subject	Session	Batch	Register Number		Total Number of students
				From	To	
25.04.24 Thursday	21EE1611 Power Electronics and Drives Laboratory	FN	B1	211421105001	211421105030	30
		AN	B2	211421105031	211421105060	30
	21EE1612 Power System Simulation Laboratory	FN	B3	211421105061	211421105092	31
		AN	B4	211421105093	211421105124	31
26.04.24 Friday	21EE1611 Power Electronics and Drives Laboratory	FN	B5	211421105125	211421105155	31
		AN	B6	211421105156	211421105325	31
	21EE1612 Power System Simulation Laboratory	FN	B1	211421105001	211421105030	30
		AN	B2	211421105031	211421105060	30
27.04.24 Saturday	21EE1611 Power Electronics and Drives Laboratory	FN	B3	211421105061	211421105092	31
		AN	B4	211421105093	211421105124	31
	21EE1612 Power System Simulation Laboratory	FN	B5	211421105125	211421105155	31
		AN	B6	211421105156	211421105325	31

*Sudhakar*  
13/4/2024

Signature of the Head of the Department

HEAD OF THE DEPARTMENT  
ELECTRICAL AND ELECTRONICS ENGINEERING  
PANIMALAR ENGINEERING COLLEGE  
POONAMALLEE, CHENNAI - 600 122



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## II YEAR END SEMESTER EXAMINATION LAB SCHEDULE APR 24

S.NO	DATE	SESSION	21EE1411-ELECTRICAL MACHINES LABORATORY - I	BATCH	TOTAL
1	25/04/2024	FN	211422105001-211422105034 Except 211422105029	A	33
		AN	211422105035-211422105067	B	33
2	26/04/2024	FN	211422105068-211422105099	C	32
		AN	211422105100-211422105132 Except 211422105107	D	32
3	27/04/2024	FN	211422105133-211422105164	E	32
		AN	211422105165-211422105185 211422105901-211422105912 Except 211422105903	F	32

*del 2*  
8/4/2024  
Signature of the Head of the Department

HEAD OF THE DEPARTMENT  
ELECTRICAL AND ELECTRONICS ENGINEERING  
PANIMALAR ENGINEERING COLLEGE  
POONAMALLEE, CHENNAI - 600 133.



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## II YEAR END SEMESTER EXAMINATION LAB SCHEDULE APR 24

S.NO	DATE	SESSION	21EE1412-DIGITAL ELECTRONICS LABORATORY	BATCH	TOTAL
1	25/04/2024	FN	211422105133-211422105164	E	32
		AN	211422105165-211422105185 211422105901-211422105912 Except 211422105903	F	32
2	26/04/2024	FN	211422105001-211422105034 Except 211422105029	A	33
		AN	211422105035-211422105067	B	33
3	27/04/2024	FN	211422105068-211422105099	C	32
		AN	211422105100-211422105132 Except 211422105107	D	32

*S. S. J.*  
3/4/2024

Signature of the Head of the Department

HEAD OF THE DEPARTMENT  
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## II YEAR END SEMESTER EXAMINATION LAB SCHEDULE APR 24

S.NO	DATE	SESSION	21EE1413 -CONTROL AND INSTRUMENTATION LABORATORY	BATCH	TOTAL
1	25/04/2024	FN	211422105068-211422105099	C	32
		AN	211422105100-211422105132 Except 211422105107	D	33
2	26/04/2024	FN	211422105133-211422105164	E	32
		AN	211422105165-211422105185 211422105901-211422105912 Except 211422105903	F	32
3	27/04/2024	FN	211422105001-211422105034 Except 211422105029	A	33
		AN	211422105035-211422105067	B	33

*Sudhakar*  
31/4/2024

Signature of the Head of the Department

HEAD OF THE DEPARTMENT  
ELECTRICAL AND ELECTRONICS ENGINEERING  
PANIMALAR ENGINEERING COLLEGE  
POORAMALLEE, CHENNAI - 605 123.